



## 10<sup>th</sup> International Science, Social Science, Engineering and Energy Conference (ISEEC 2019)

<http://i-seec2019.rmuti.ac.th/>

### Decision Letter

Ref No. : ETE04-077  
Author(s) : Mr. Bancha Burapattanasiri  
Paper Title : Multifunction CMOS signal converter circuit in current-mode

Dear **Mr. Bancha Burapattanasiri**,

We are pleased to inform you that your paper entitled "**Multifunction CMOS signal converter circuit in current-mode**" is **Accepted for presentation in the 10<sup>th</sup> International Science, Social Science, Engineering and Energy Conference (ISEEC 2019) on November 20 – 23, 2019 at Rajamangala University of Technology Isan Sakon Nakhon Campus, Sakon Nakhon, Thailand.** Your paper has gone through a review process and in conclusion, you are invited to present your research findings at the conference.

As a paper presenter at the conference you are required to complete the registration as indicated in the website.

During the conference, you are expected to deliver in full your research findings in a span of 15 minutes, in which 3 minutes discussions is included.

Thank you very much for your great contribution to our conference, ISEEC 2019.

Best Regards,

Assoc. Prof. Kosit Sreephuthurn  
Chairman, ISEEC 2019.  
Vice President, Sakon Nakhon Campus.

## Multifunction CMOS signal converter circuit in current-mode

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### ABSTRACT

This paper presents a multifunction CMOS signal converter circuit in current-mode. Under Conditions 0.5 $\mu$ m level 3 CMOS technology, and  $\pm 2.0$  V low voltage. The working operation of input receiver and output current mode responded at high frequency. The main structure consists of block set: CMOS current-mode squaring function, and current mirror circuit. The performances of proposed circuit are investigated through PSpice. The proposed circuit can be performed many functions including, squared circuit, rectifier circuit, and vector summation circuit, respectively. The simulation circuit responds a wide current, high precision, and low power losses. The advantage of proposed circuit has not distortion of the output signals.

**Keywords:** *multifunction, CMOS, current-mode*